

**Fig. 1A**

**Fig. 1**

**———— Power Path**  
**———— Secondary Connections**  
**.....-O Inputs/Outputs to PLC**

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Fig. 1A1

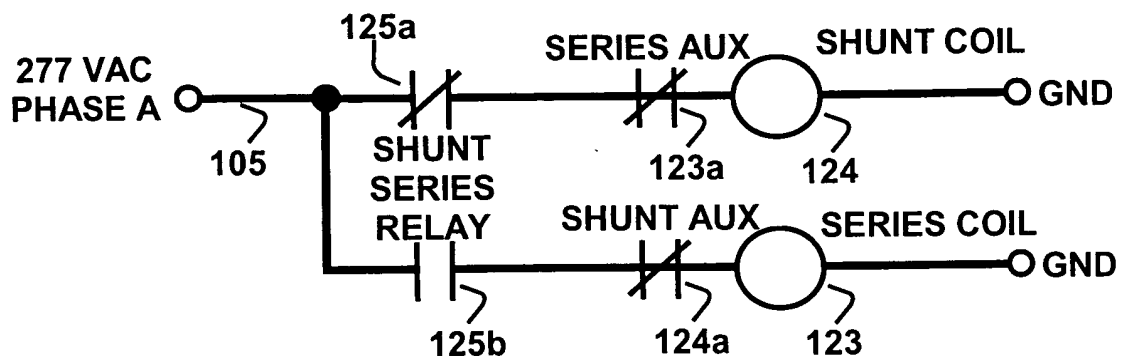


Fig. 1A2

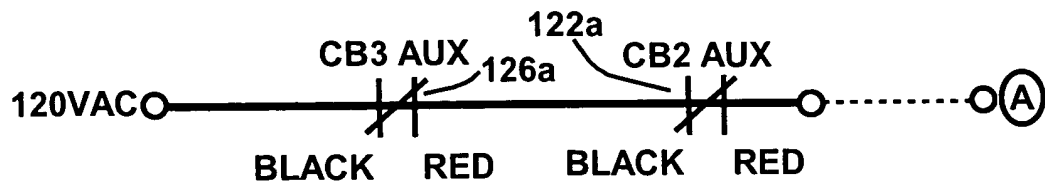
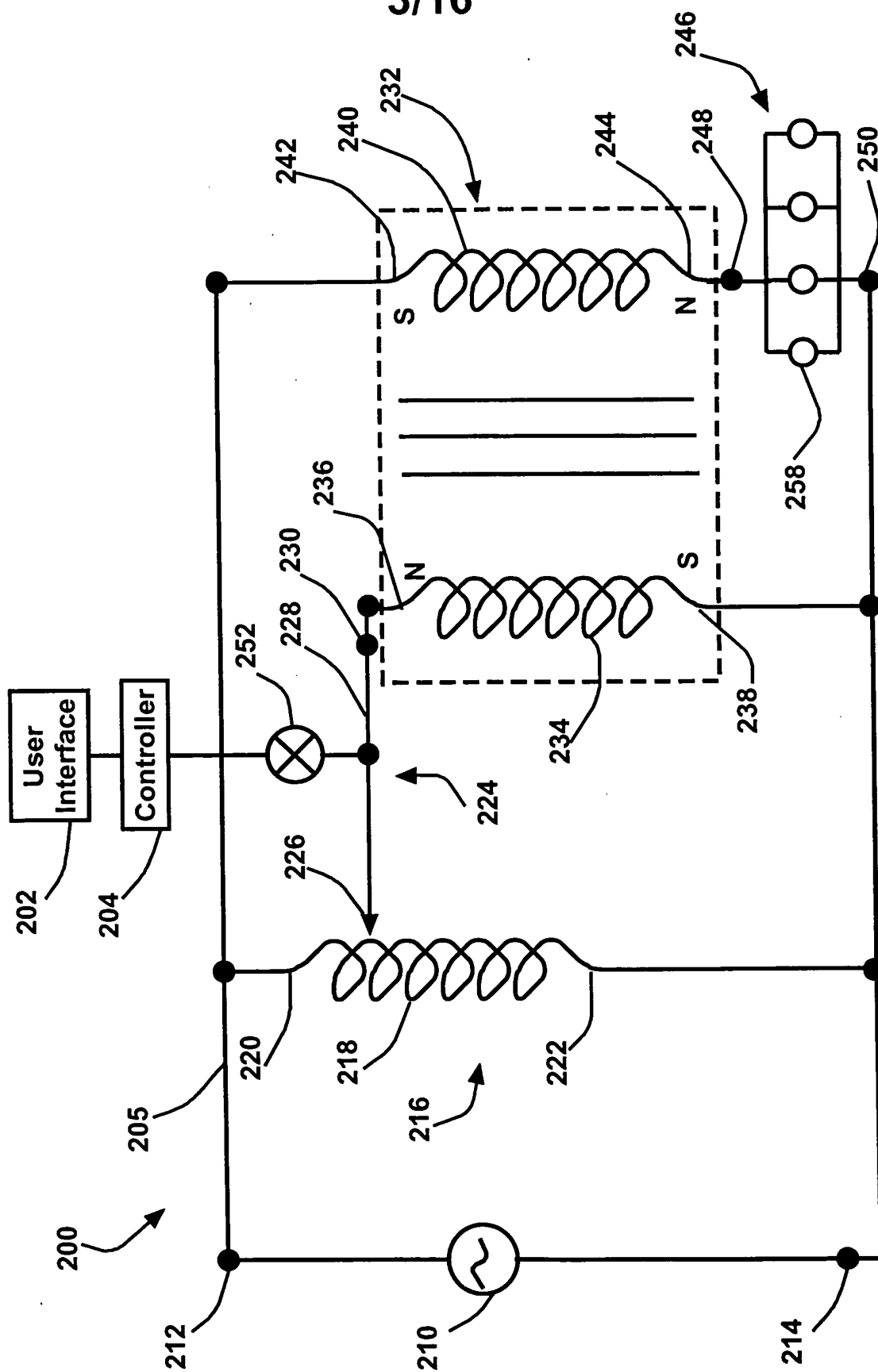


Fig. 1A3



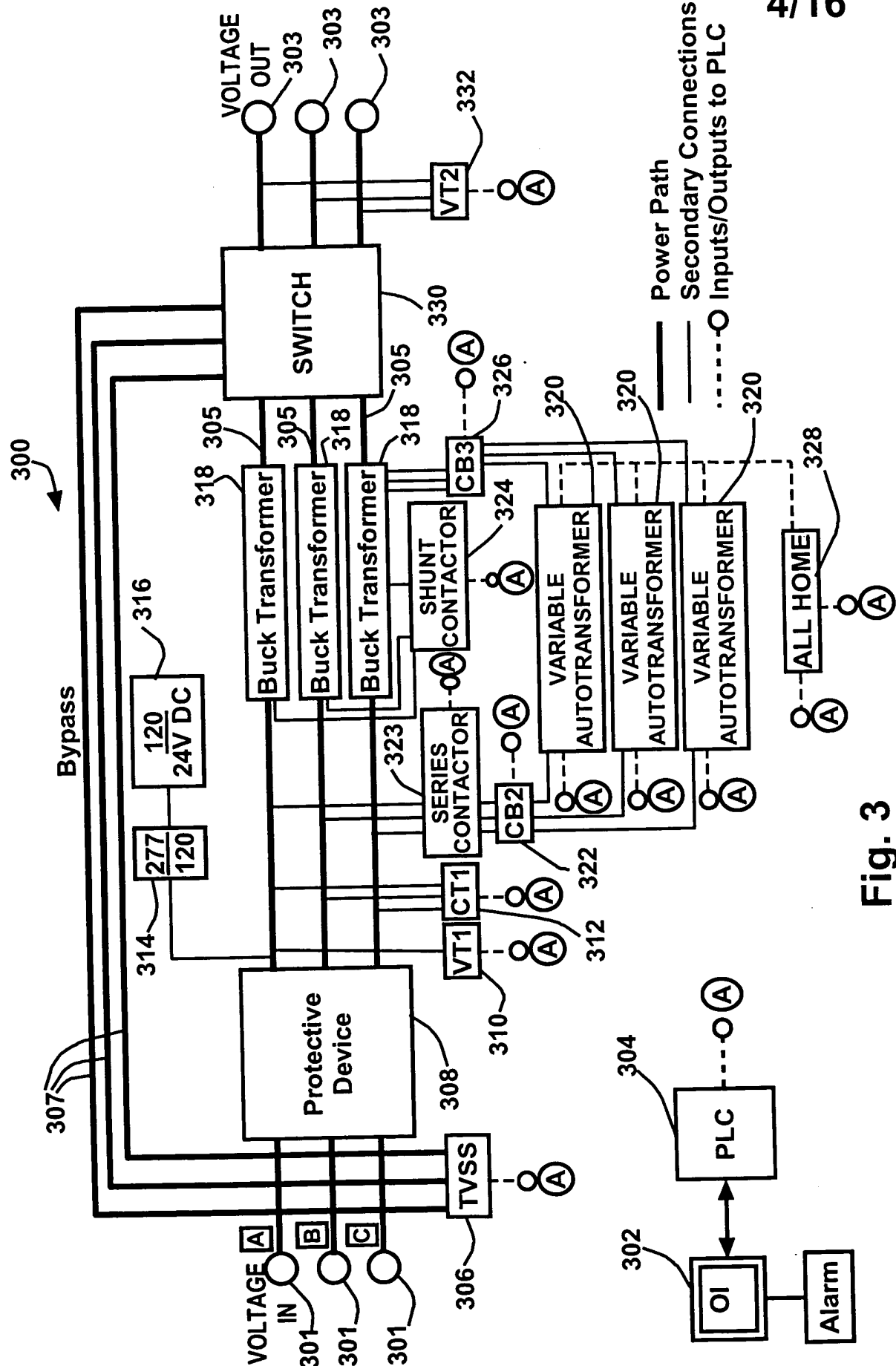


Fig. 3

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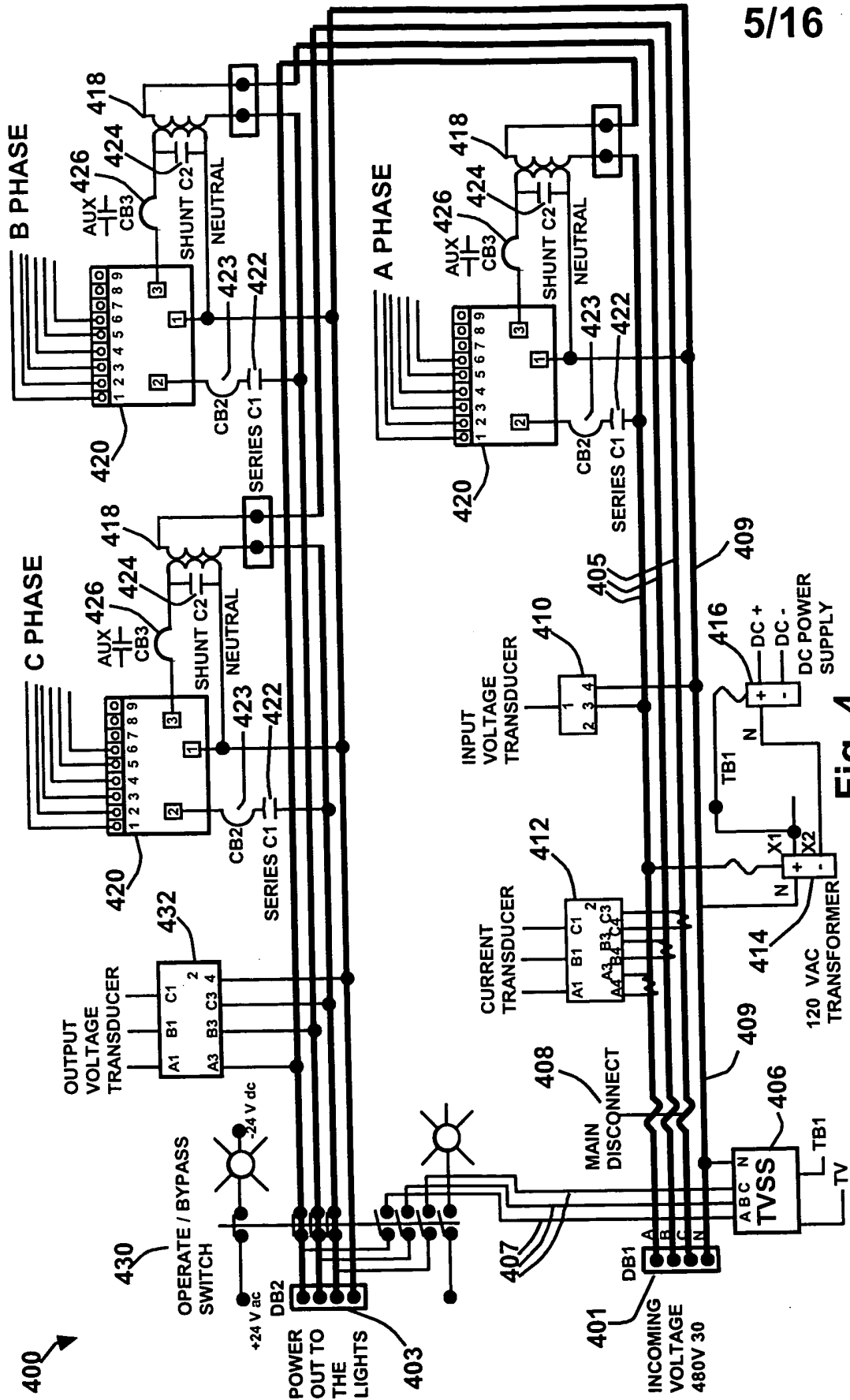
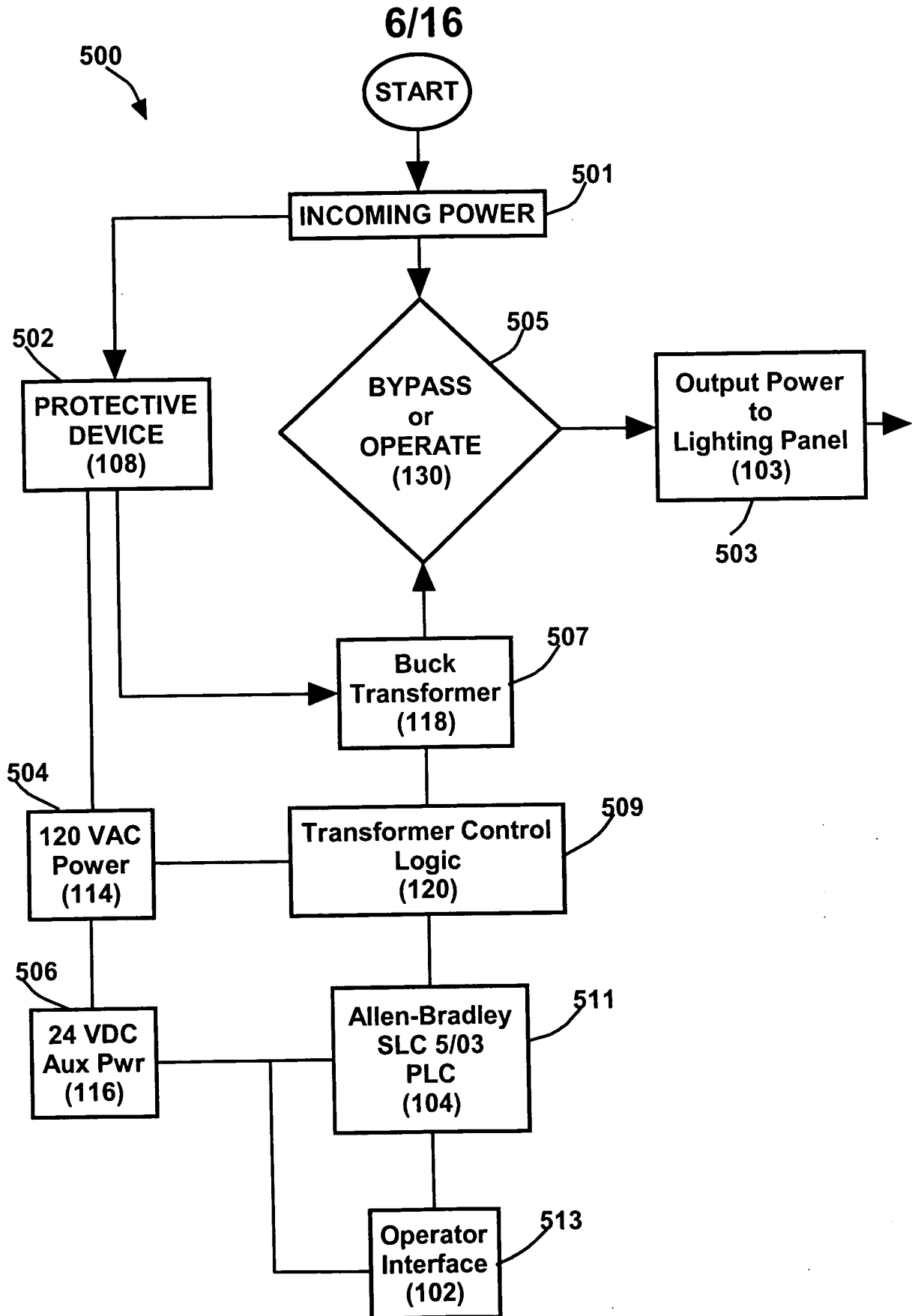


Fig. 4



**Fig. 5**

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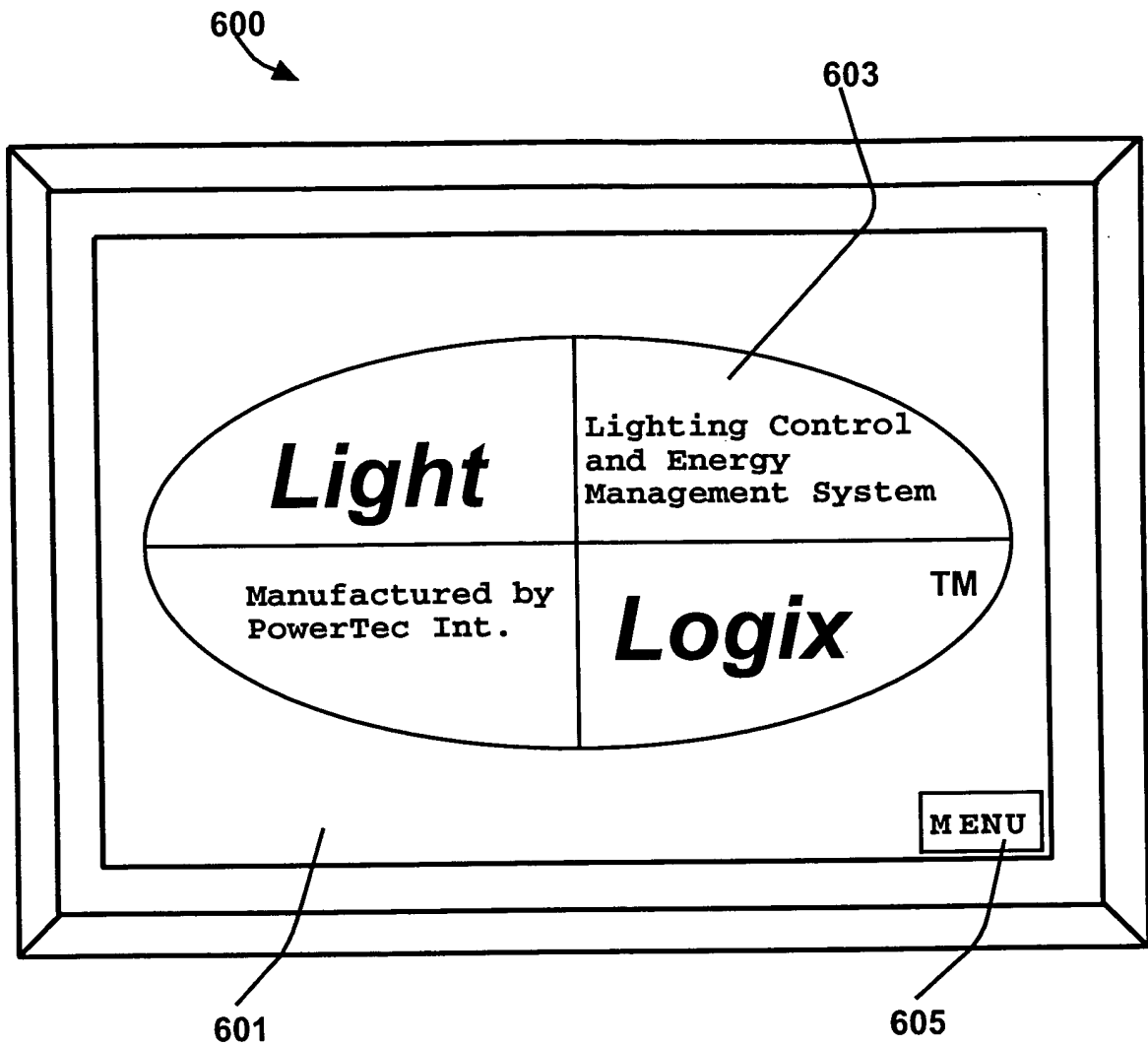


Fig. 6

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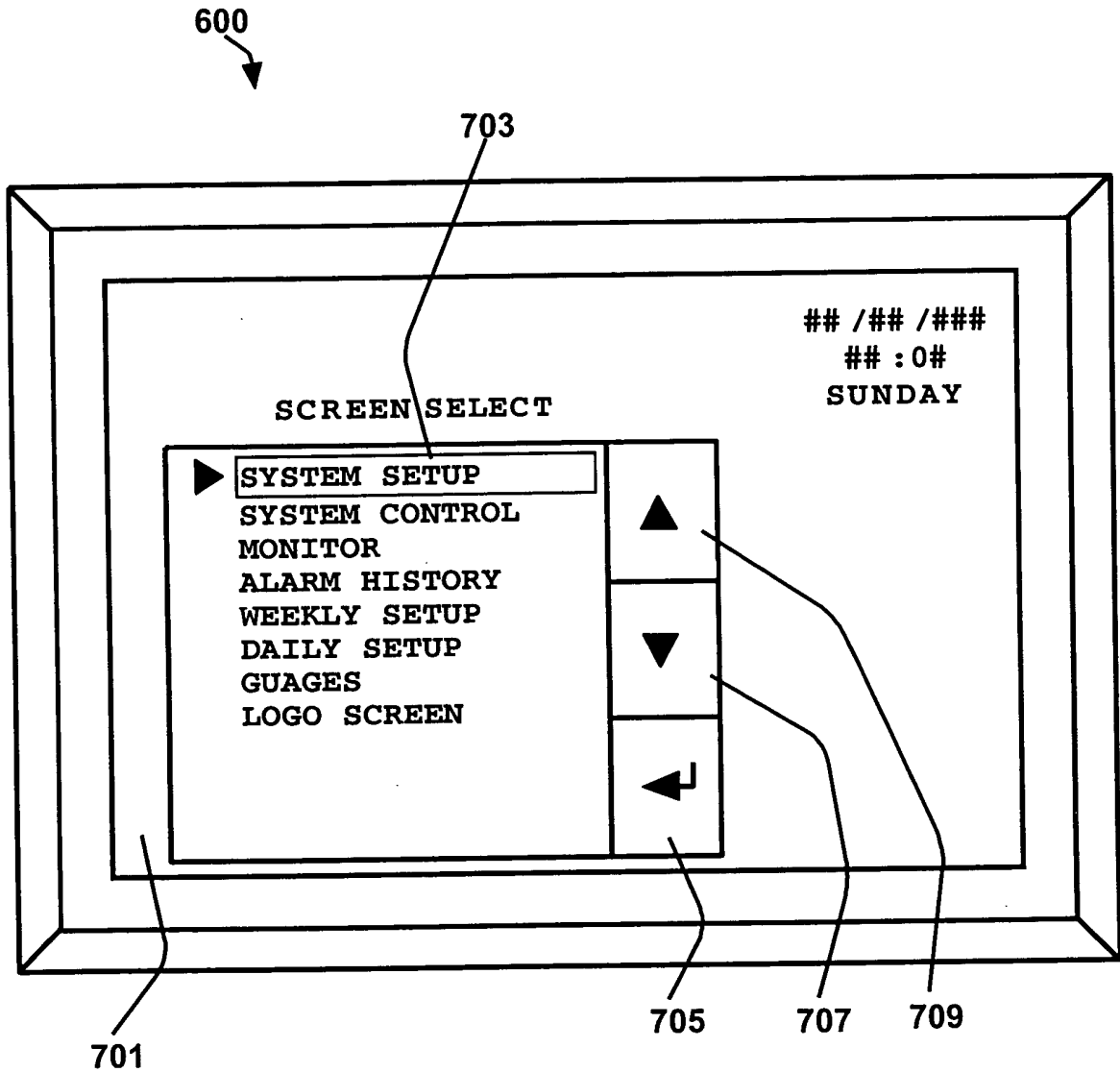


Fig. 7



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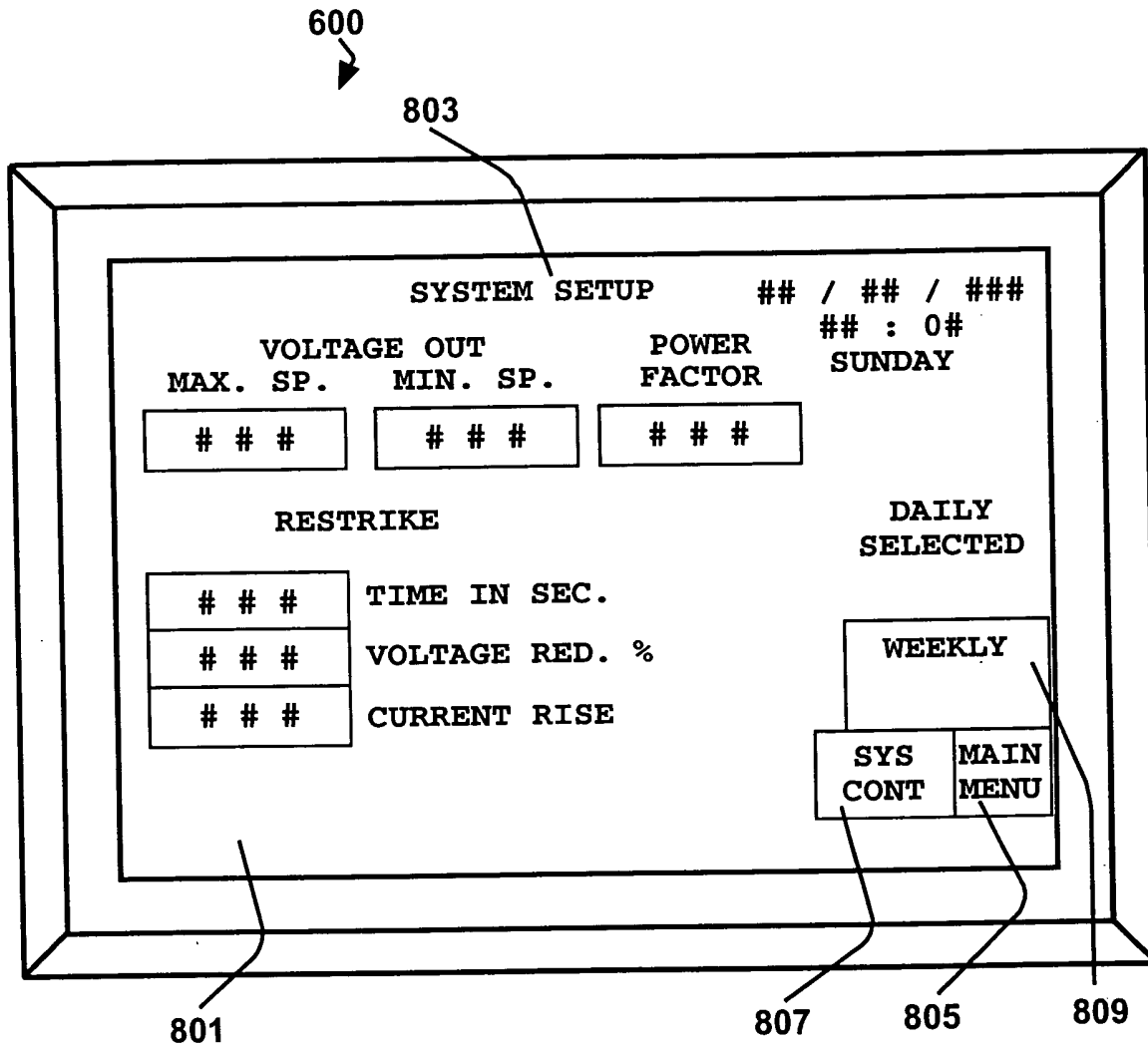


Fig. 8

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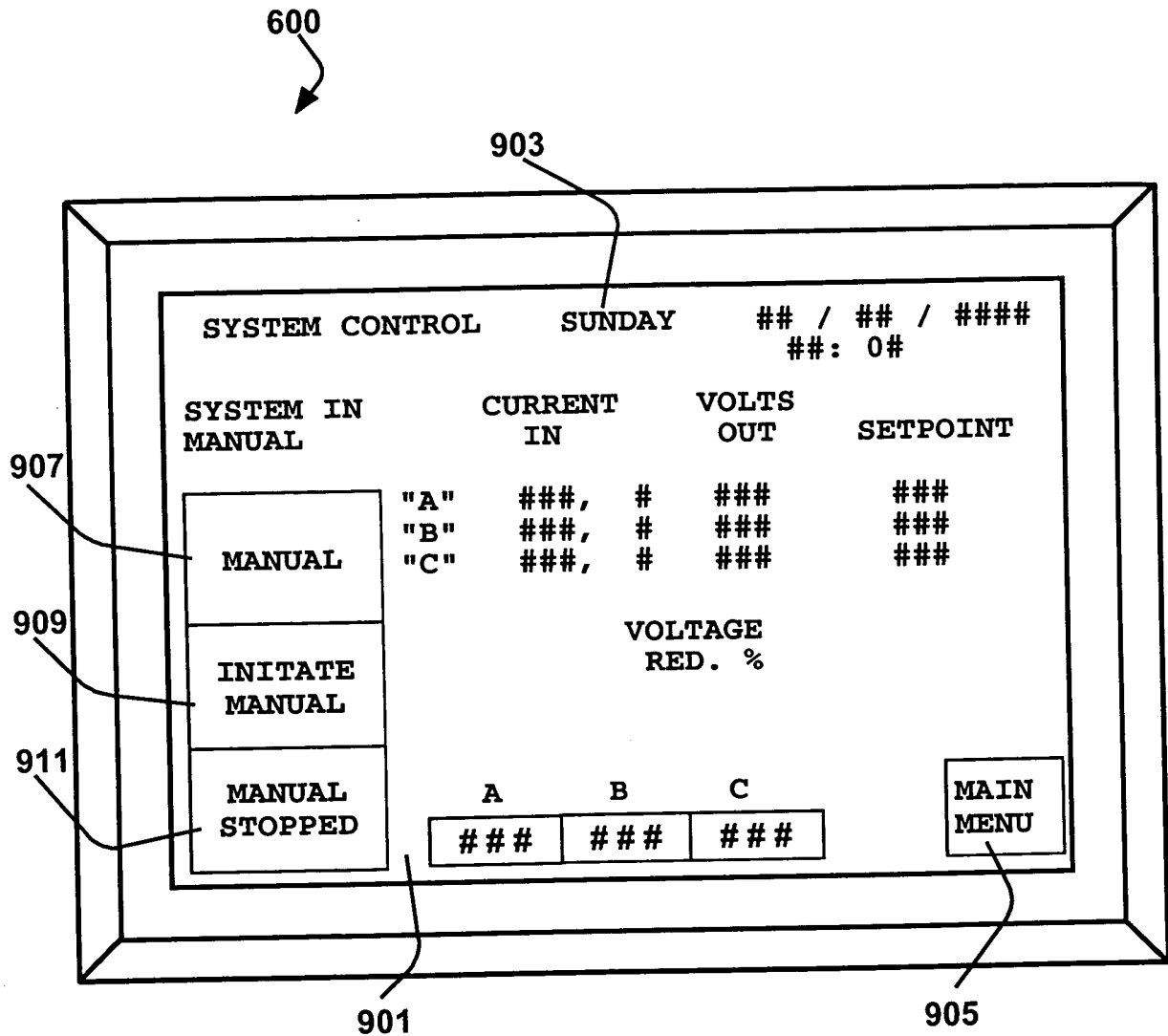


Fig. 9

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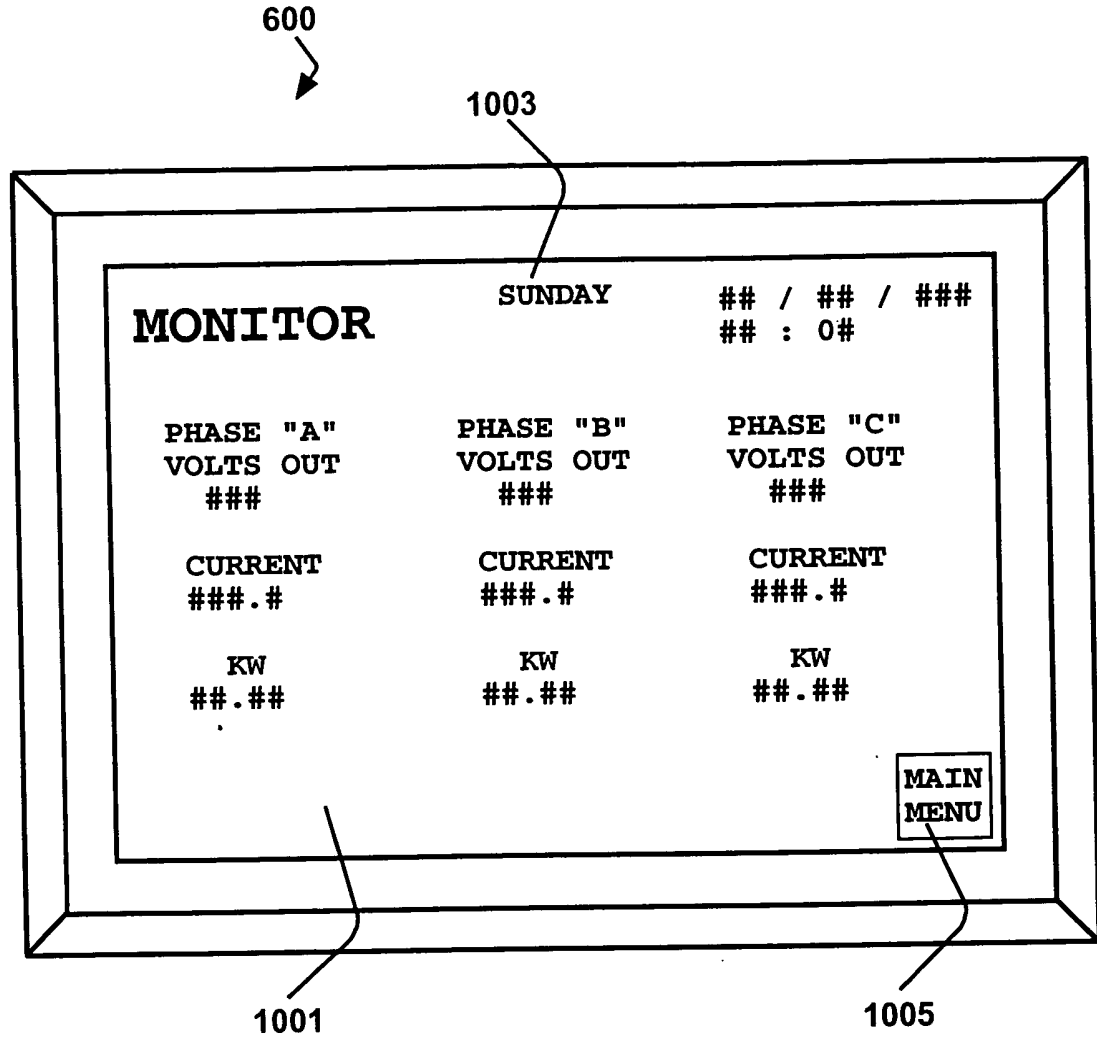


Fig. 10

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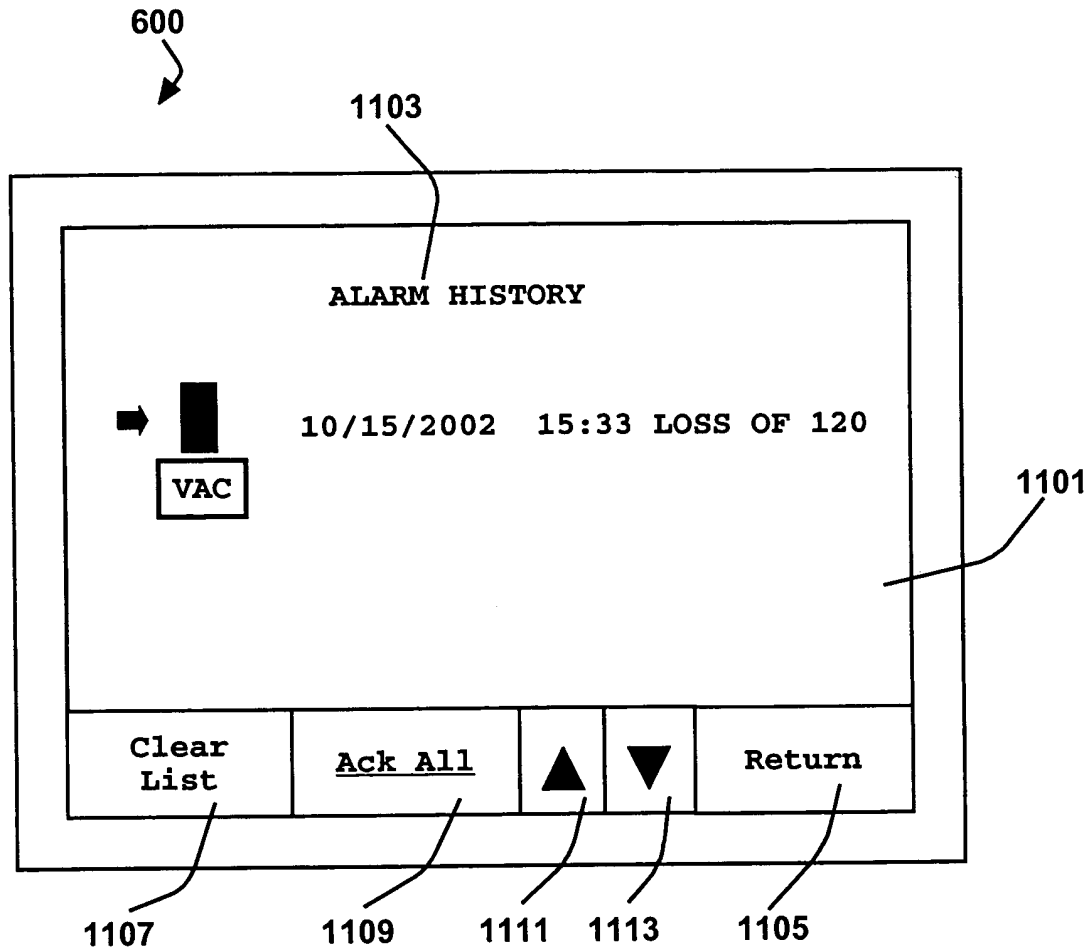


Fig. 11

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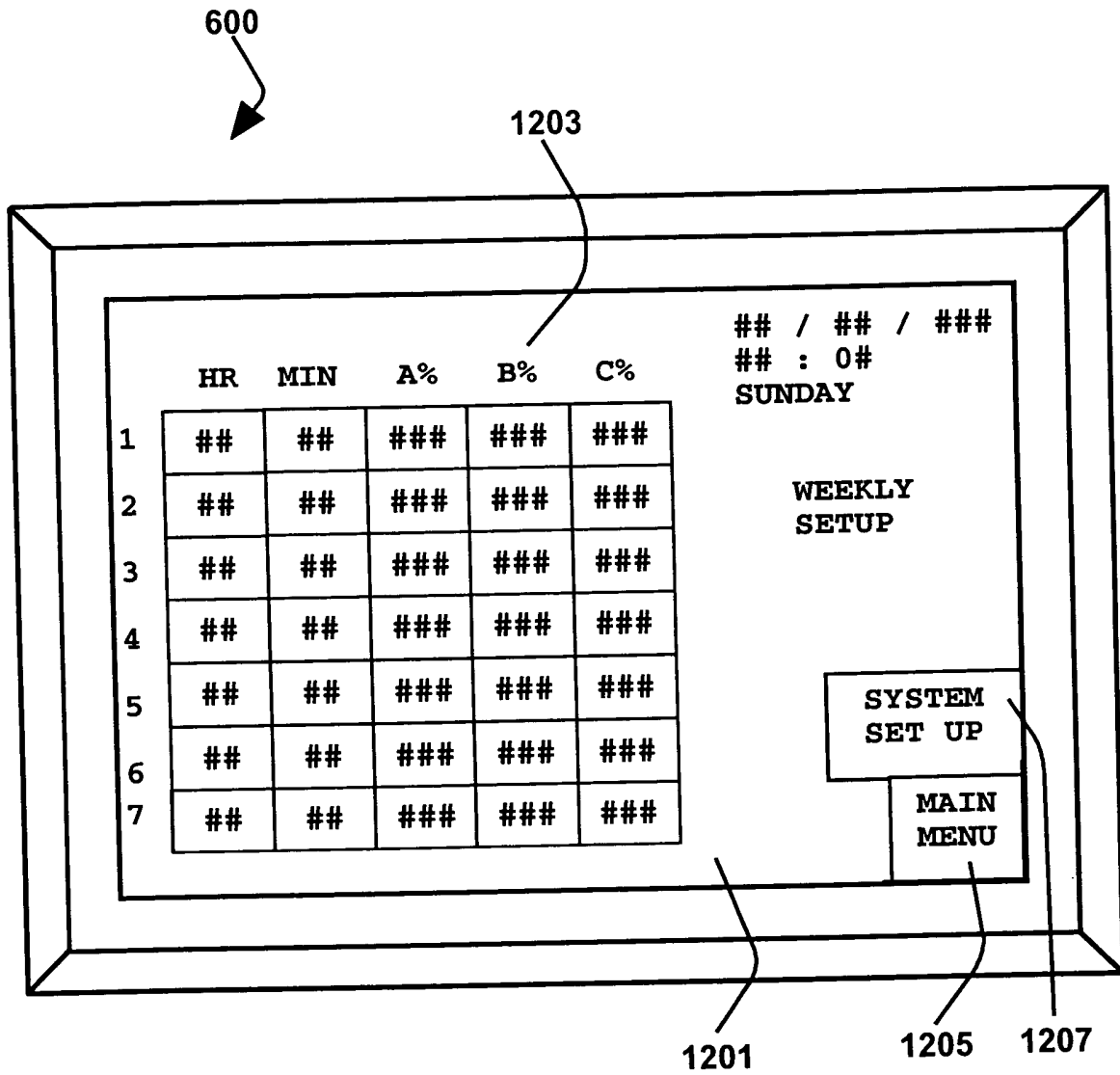


Fig. 12

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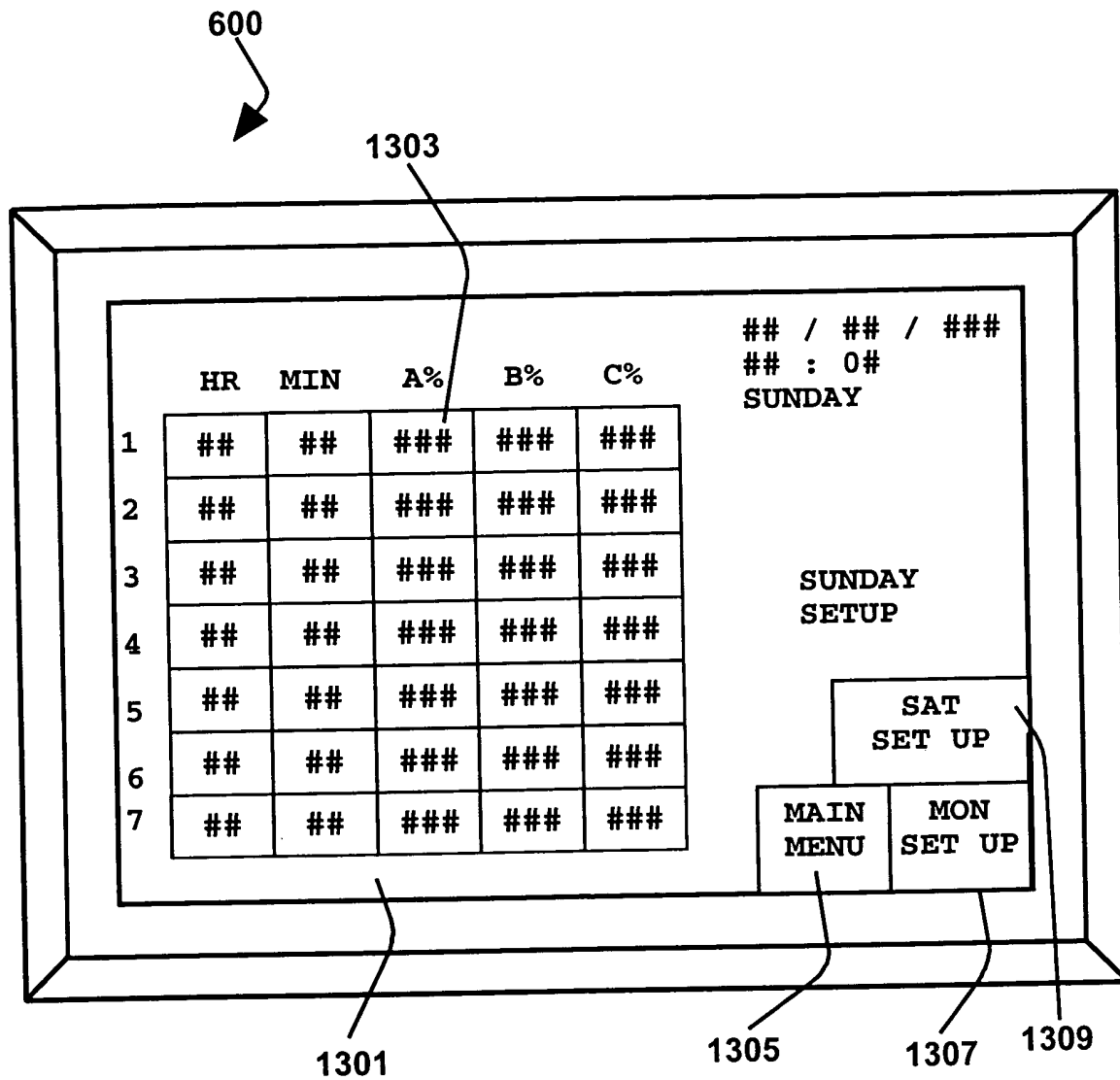


Fig. 13

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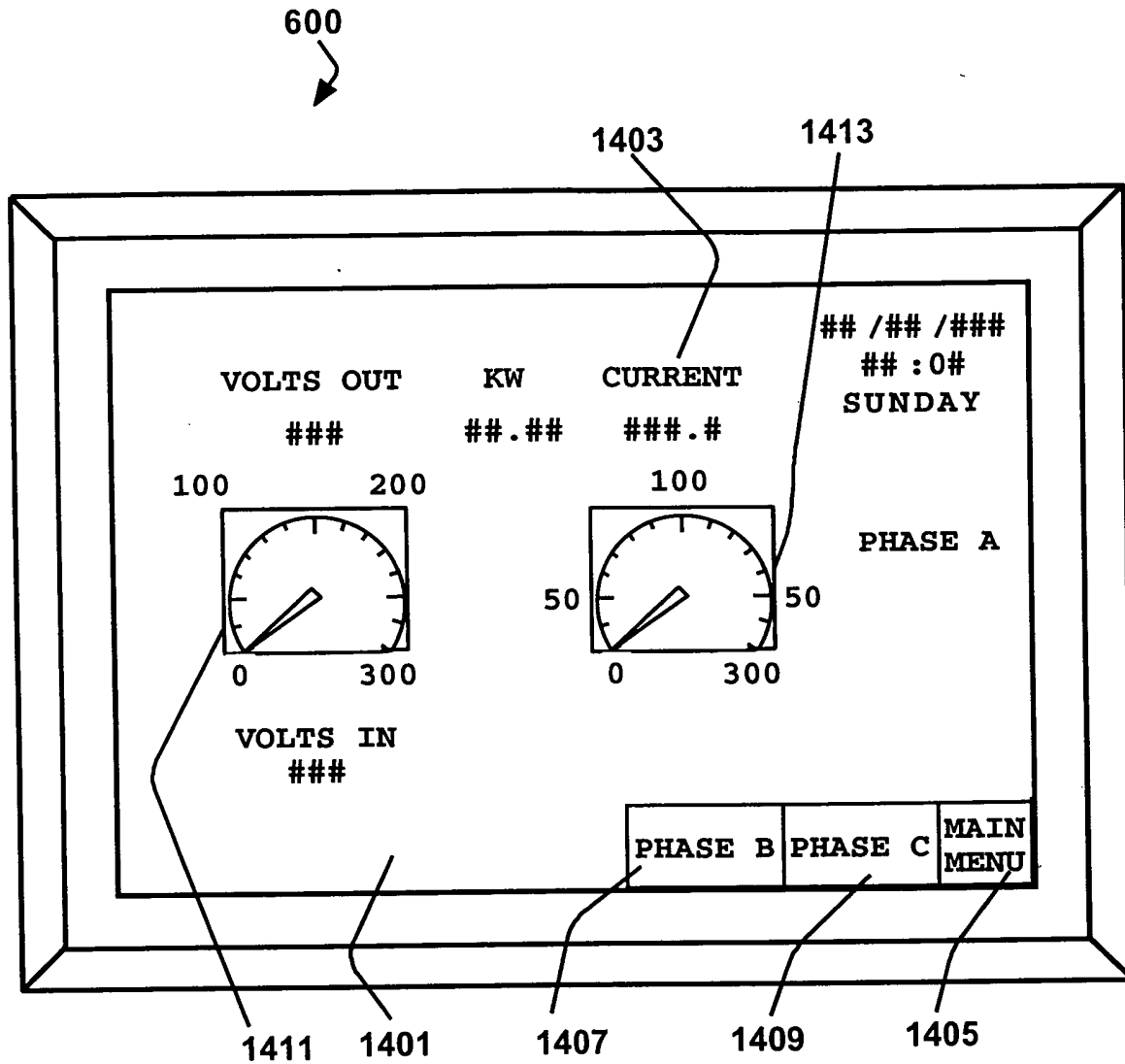


Fig. 14

